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ClearSpeed CTO Receives IEEE Computer Society Golden Core Award

John Gustafson one of select group recognized for commitment and service to the society

San Jose, CA – April 24, 2007 – John Gustafson, chief technical officer for High Performance Computing (HPC) for ClearSpeed Technology (LSE:CSD), has been named a recipient of the IEEE Computer Society Golden Core Award which recognizes IEEE Computer Society members for their long-standing membership and outstanding service. Each year the IEEE Awards Committee selects up to a maximum of 50 recipients out of the more than 100,000 current IEEE Computer Society members and permanently includes the names in the Golden Core Member master list.

The Golden Core is the highest level of membership designation in the IEEE Computer Society. Gustafson first became a member of the IEEE Computer Society in 1982.

“John is an experienced and highly-regarded engineer who has contributed a great deal to the IEEE Computer Society and its programs,” said Dr, Michael R. Williams, 2007 President of the IEEE Computer Society. "The Golden Core Award is given to those long-standing members or staff for service to the Society. Awardees must have previously received one of our other awards for service and demonstrated active involvement in one of our programs.”

Gustafson was also recently recognized for his work on replicating the first fully-operational electronic digital computer, the Atanasoff-Berry Computer (ABC). Last fall, he received the John Atanasoff Award from President Georgi Parvanov of Bulgaria and the IEEE Computer Society Outstanding Contribution Award for his contribution to the ABC.

Gustafson joined ClearSpeed in 2005 after leading HPC efforts at Sun Microsystems. He has 33 years experience using and designing compute-intensive systems, including the first matrix algebra accelerator and, while at Floating Point Systems, the first commercial massively-parallel cluster.

His pioneering work on a 1024-processor nCUBE at Sandia National Laboratories created a watershed in parallel computing, for which he received the inaugural Gordon Bell Award. He also

has received three R&D 100 Awards for innovative performance models, including the model commonly known as Gustafson's Law or Scaled Speedup.

He received his B.S. degree from Caltech and his M.S. and Ph.D. degrees from Iowa State University, all in Applied Mathematics.

About ClearSpeed

ClearSpeed Technology is a semiconductor company that develops massively parallel coprocessors and accelerator boards delivering unmatched performance per watt for high performance computing applications on industry-standard systems. ClearSpeed has offices in San Jose, California and Bristol, UK and has 84 patents granted and pending. For more information, visit www.clearspeed.com.

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